PA120A



High Speed Power Amplifier PA120A

• Bandwidth: DC-1,5 MHz • Slew-Rate: $250 \text{ V/}\mu\text{s}$ • Load current: $> 10 \text{ A}_P$ • Offset: < 3 mV

Sophisticated limiting and protection concept



Application Range:

- Research
- Development (in particular power electronics)
- · Pulse physics
- Component testing
- · Quality assurance
- Measurement and test engineering

PA120A is a high speed DC-coupled power amplifier featuring power bandwidth of 1,5MHz.

The output stage of PA120A provides output voltages ranging from -30V to +30V, whereas load currents of \pm 4A and peak load currents up to \pm 10A are available.

The amplifier comes with two input channels; their signals are added together. This allows e.g. for providing an AC-signal with a DC-offset. Another example would be superimposing of the wanted signal by a disturbing signal. Input resistance $(50\Omega / 10k\Omega)$ as well as coupling mode (AC / GND /DC) and gain (x1 / x10) can be set for each input channel separately.

LED indicators at the inputs (LIMIT + / LIMIT -) signalise overmodulation of the input stage.

The power output stage is protected against overload and overtemperature. The supply voltage of the output stage is varied depending on junction temperature of its transistors (Tj LIMIT + / Tj LIMIT -). This method allows for achieving high output currents even at small output voltages.

Monitoring of the junction temperature also makes a fast current limitation unnecessary, thus allowing for very high peak output currents (> 10A). If either the junction temperature exceeds 125°C (Tj OVER) or the maximum heatsink temperature is exceeded (Ths OVER), the output of the amplifier will be disconnected.

If the output voltage level exceeds its positive or negative limit, the appropriate output limit indicator LED lights up (LIMIT + / LIMIT -). The output voltage will be then reduced, preventing overmodulation of the power output stage.

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• Technical Specifications

Parameter	Symbol	Min	Тур	Max	Unit	Comment
Absolute Maximum Ratings:						
Input Voltage (R_I 10k Ω)	V _{I10}	-90		+90	V_P	
Input Voltage (R_I 50 Ω)	V_{I50}		15		V_{RMS}	
Floatwice Chause to vietice						
Electrical Characteristics:	1/	-30		+30	V _P	
Input Voltage (R_I 10k Ω , GAIN x1)	V_{I10-1}	-30		+30	V P	
Input Voltage	V _{I10-10}	-3		+3	V _P	
$(R_I 10k\Omega, GAIN \times 10)$	V I10-10	-5		-3	V P	
Input Voltage	V _{I50-1}	-30		+30	V _P	max. 15V _{RMS} !
$(R_I 50\Omega, GAIN \times 1)$	V 150-1			130	VP	ITIOX: 15 V RMS :
Input Voltage	V ₁₅₀₋₁₀	-3		+3	V _P	
$(R_I 50\Omega, GAIN \times 10)$	- 150-10				- '	
Lower Cutoff-Frequency AC	f (-3dB)		15		Hz	
Output Voltage	Vo	-30		+30	V_P	R _L ≥7.5Ω, f≥10kHz
Load Current	I_{ODC}	2	2,2		Α	f=0Hz
Load Current	I_{OAC}	4	4,5		A_P	f≥10kHz, square
Load Current	I_{OAC}	6.3	7		A_P	f≥10kHz, sine
Pulsed Load Current	I_{OP}	10	20		A_P	t _P ≤20µs, D≤0.1
	_					
Output Resistance	Ro		0.1		Ω	
Output Resistance Monitor Out	R _{OM}		51		Ω	
Francisco Decreases	f (24D)		0.15		MII-	B 500
Frequency Response	f (-3dB)		01.5		MHz	$R_L=50\Omega$
Frequency Response BW-LIMIT	f (-3dB)	222	0300		kHz	$R_L=50\Omega$
Slew-Rate	SR	200	250		V/µs	$R_L=50\Omega$
Output Offset Voltage	ΔV_{OO}	-3	1.5	+3	mV	
Suspect offices	<u> </u>		1.5	1.3		
Mains Voltage	V _{LINE}	207	230	253	V_{AC}	
Mains Frequency	f _{LINE}	45	50	65	Hz	
Power Consumption	P _{LINE}	25		300	W	
Input Current	I _{LINE}			1.6	Α	
Mains Fuse				T3.15A		UL 248
Applicate Tomorosantonio	_	0	20	25	00	
Ambient Temperature (operation)	T _{AMB}	0	20	35	°C	
Storage Temperature	T _S	-25		+85	°C	
Width	W		470		mm	
Height	H		153		mm	
Depth	D		300		mm	
Weight	m		10.5		kg	

Specifications are subject to change without notice.

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